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Docket No.: M4065.0381/P381-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Leonard Forbes et al.

Application No.: 10/765,477

Confirmation No.: 9433

Filed: January 28, 2004

Art Unit: 2826

For: TECHNIQUE TO MITIGATE SHORT
CHANNEL EFFECTS WITH VERTICAL
GATE TRANSISTOR WITH DIFFERENT
GATE MATERIALS

Examiner: Not Yet Assigned

STATUS INQUIRY

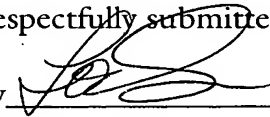
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive an Office Action from the Patent and Trademark Office.

Dated: March 7, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Ryan H. Flax

Registration No.: 48,141

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicant